



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shuichi Kikuchi et al.                      Art Unit : 2813  
Serial No. : 10/651,855                                      Examiner : Thanh Nguyen  
Filed : August 29, 2003  
Title : SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING IT

**MAIL STOP RCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT IN REPLY TO ACTION OF MAY 16, 2005

Prior to examination, please amend the above-identified application as follows:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

August 16, 2005

Date of Deposit

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